## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of HATAE et al. Application Number: To Be Assigned		)
		)
		)
Filed: Concurrently Herewith		)
For:	SEMICONDUCTOR INTEGRATED CIRCUIT AND	)

10/080578 10/080578

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

## NOTICE OF PRIORITY UNDER 35 U.S.C. § 119 AND THE INTERNATIONAL CONVENTION

Sir:

In the matter of the above-captioned application for a United States patent, notice is hereby given that the Applicant claims the priority date of May 31, 2001, the filing date of the corresponding Japanese patent application 2001-163575

The certified copy of corresponding Japanese patent application 2001-163575 will be submitted as soon as it is received from Applicant. Acknowledgment of receipt of the certified copy is respectfully requested in due course.

Respectfully submitted,

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